

# CIRCUIT THEORY APPROACH OF A SUBTHRESHOLD MOS NEURAL NETWORK WORKING AS WTA

**Ruxandra L. COSTEA, Victor E. BUCATA and Corneliu A. MARINOV**

Department of Electrical Engineering

Polytechnic University of Bucharest,

Spl. Independentei 313, 060032, Bucharest, Romania.

Email: [ruxandra.costea@upb.ro](mailto:ruxandra.costea@upb.ro), [victor.bucata@upb.ro](mailto:victor.bucata@upb.ro), [cmarinov@rdslink.ro](mailto:cmarinov@rdslink.ro)

We study the classical Lazzaro circuit, the analog MOS network which can perform rank selection tasks. The circuit has  $N$  currents as inputs and  $N$  voltages as corresponding outputs and is of feedback type with a common control current. Its use in neural computation is based on the simplicity of VLSI manufacture and on large applications in low power signals as those of biological sensors. Therefore, we discuss the dynamic model built with MOS transistors in subthreshold region. We emphasize on rigorous conditions keeping the devices inside that region for all transient duration, a consistency requirement which seems neglected so far. Then we study the W(inner) T(ake) A(ll) function of the circuit in the following steps: first, we prove lower and upper bounds for each output voltage rank, by indicating the corresponding list of input currents. Second, we prove that the maximum of  $u_2$  (second voltage component) surpasses the minimum of  $u_1$ . This apparently voids the WTA functioning. Third, we show that this mismatch can be squeezed as much as we want by a proper choose of maximum input allowed and the control current value. These parameters depend on the list length and density. Hence, our circuit can select the maximum of a list with a prescribed precision by signaling the rank over a computable threshold.

## References

- [1]. J. Lazzaro, R. Ryckebusch, M.A. Mahowald, and C.A. Mead, "Winnertake- all networks of  $O(n)$  complexity," in *Advances in Neural Information Processing Systems*, Vol. 1, D.S. Touretzky, Ed. Los Altos, CA: Morgan Kaufmann, 1989.
- [2]. A.G. Andreou, K.A. Boahen, P.O. Pouliquen, A. Pavasovic, R.E. Jenkins and K. Strohhahn, "Current-mode subthreshold MOS circuits for analog VLSI neural systems," *IEEE Trans. Neural Networks*, Vol. 2, No. 2, pp. 205-213, 1991.
- [3]. J.A. Starzyk and X. Fang, "CMOS current mode winner-take-all circuit with both excitatory and inhibitory feedback," *Electronics Letters*, Vol. 29, No. 10, pp. 908-910, 1993.
- [4]. B. Sekerkiran and U. Cilingiroglu, "Improving the resolution of Lazzaro winner-take-all circuit," *International Conference on Neural Networks*, Vol. 2, pp. 1005-1008, 1997.
- [5]. C.A. Marinov, R.L. Costea and V.E. Bucata, "On the invariance of Lazzaro circuit model", 2012 The 55th International Midwest Symposium on Circuits and Systems (MWSCAS), pp.1188-1191, Boise, USA, August 2012.

## Acknowledgement

The work has been co-funded by the Sectoral Operational Programme Human Resources Development 2007-2013 of the Romanian Ministry of Labour, Family and Social Protection through the Financial Agreement POSDRU/89/1.5/S/62557.